

Surface Mount Ultrafast Rectifiers

FEATURES

- Very low profile, typical height of 1.1mm
- Excellent high temperature stability
- Glass passivated chip junction
- Controled avalanche characteristics
- Low leakage current
- High forward surge capability
- Moisture sensitivity level: level 1, per J-STD-020
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

TYPICAL APPLICATIONS

For use in high voltage, high frequency power factor corrections, switching mode power supplies, freewheeling diodes and secondary dc to dc rectifications

MECHANICAL DATA

Case: TO-277A (SMPC)

Molding compound, UL flammability classification rating 94V-0 Packing code suffix "G" means green compound (halogen-free) **Terminal:** Matte tin plated leads, solderable per JESD22-B102 Meet JESD 201 class 1A whisker test **Polarity:** Indicated by cathode band **Weight:** 95 mg (approximately)

					ted)		
	PARAMETER			TPUH6D		TPUH6J	
Marking code				UH6D UH6J			
Maximum repetitive peak reverse voltage				200 600		V	
Maximum average forward rectified current			6			Α	
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load			80			А	
Tes	t condition		TYP	MAX	TYP	MAX	V
1 - 2 4	T _J =25°C		0.80	-	1.98	-	
IF=3A	T _J =125°C	V _F	0.65	-	1.23	-	
1 -6 4	T _J =25°C		0.87	1.05	2.45	3.00	
I _F =0A	T _J =125°C		0.73	0.90	1.59	1.80	
Maximum reverse current @ rated VR T _J =25°C T _J =125°C			10			μA	
			200				
Maximum reverse I_F =0.5A, I_R =1A, I_{RR} =0.25Arecovery time I_F =1A, di/dt=-50A/µs, V_R =30V			25			ns	
			45				
Typical thermal resistance			12			°C/W	
			80				
Typical junction capacitance ⁽⁴⁾			50			pF	
Operating junction temperature range			- 55 to +175			°C	
Storage temperature range			- 55 to +175			°C	
	Tes I _F =3A I _F =6A C C A, I _R =1A, I _{RR} =	Test condition $I_F=3A$ $T_J=25^{\circ}C$ $T_J=125^{\circ}C$ $T_J=125^{\circ}C$ $I_F=6A$ $T_J=25^{\circ}C$ $T_J=125^{\circ}C$ $T_J=125^{\circ}C$ C $T_J=125^{\circ}C$ A, $I_R=1A$, $I_{RR}=0.25A$	$\begin{array}{c c} & & & & \\ \hline Test \ condition \\ \hline I_F=3A & \hline T_J=25^\circ C \\ \hline T_J=125^\circ C \\ \hline I_F=6A & \hline T_J=25^\circ C \\ \hline T_J=125^\circ C \\ \hline T_J=125^\circ C \\ \hline \end{array} \\ \hline \\ \hline \\ C \\ C \\ \hline \\ A, \ I_R=1A, \ I_{RR}=0.25A \\ \hline \end{array} \\ \begin{array}{c} & & \\ I_R \\ \hline \\ I_R \\ \hline \end{array} \\ \hline \end{array}$	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $	$ \begin{array}{c c c c c c c c c } \hline V_{RRM} & 200 & 60 \\ \hline I_{F(AV)} & 6 \\ \hline I_{F(AV)} & & 80 \\ \hline I_{rot} \\ \hline I_{rot} \\ \hline I_{r} = 3A & \hline T_{J} = 25^{\circ}C \\ \hline T_{J} = 125^{\circ}C & \\ \hline T_{J} = 125^{\circ}C & \\ \hline I_{F} = 6A & \hline T_{J} = 25^{\circ}C & \\ \hline T_{J} = 125^{\circ}C & \\ \hline I_{F} = 6A & \hline T_{J} = 125^{\circ}C & \\ \hline I_{F} = 6A & \hline T_{J} = 125^{\circ}C & \\ \hline I_{R} & & 10 \\ \hline 0.87 & 1.05 & 2.45 & \\ \hline 0.73 & 0.90 & 1.59 & \\ \hline$	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $

Note 1: Pulse test with PW=300µs, 1% duty cycle

Note 2: Mounted on FR4 PCB with 16mm x 16mm Cu pad area

Note 3: Free air, mounted on recommned pad

Note 4: Measured at 1 MHz and Applied V_R =4.0 V







TO-277A (SMPC)

-O Cathode 3

Anode 1 O

Anode 2 O-





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ORDERING INFORMATION					
PART NO.	PACKING CODE	PACKING CODE SUFFIX	PACKAGE	PACKING	
TPUH6x	S1	G	SMPC	1,500/ 7" Plastic reel	
(Note 1, 2)	ote 1, 2) S2	SMPC	6,000/ 13" Plastic reel		

Note 1: "x" defines voltage from 200V (TPUH6D) to 600V (TPUH6J)

Note 2: Whole series with green compound

EXAMPLE				
PREFERRED PART NO.	PART NO.	PACKING CODE	PACKING CODE SUFFIX	DESCRIPTION
TPUH6J S1G	TPUH6J	S1	G	Green compound

RATINGS AND CHARACTERISTICS CURVES

(T_A=25°C unless otherwise noted)

100

80

60

40

20

0

1

PEAK FORWARD SURGE CURRENT (A)

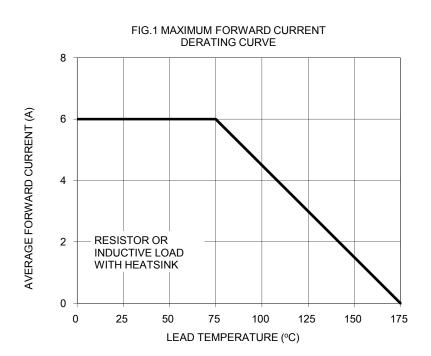


FIG. 3 MAXIMUM NON-REPETITIVE FORWARD PEAK

SURGE CURRENT

10

NUMBER OF CYCLES AT 60 Hz

8.3ms Single Half Sine Wave

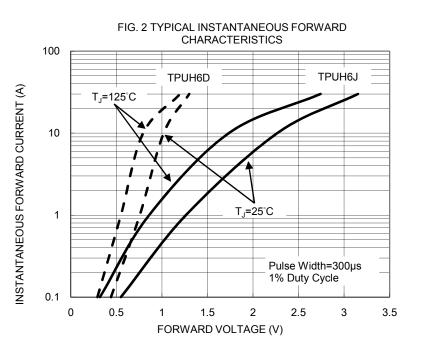
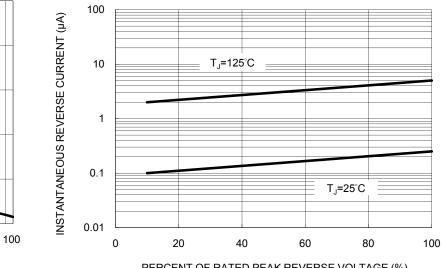


FIG. 4 TYPICAL REVERSE CHARACTERISTICS



PERCENT OF RATED PEAK REVERSE VOLTAGE (%)



TPUH6D thru TPUH6J

Taiwan Semiconductor

FIG. 5 TYPICAL JUNCTION CAPACITANCE

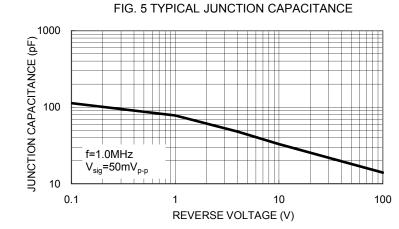
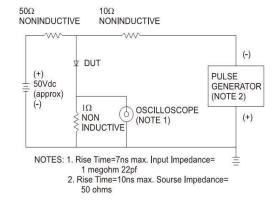
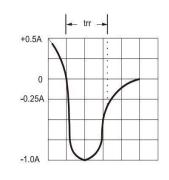
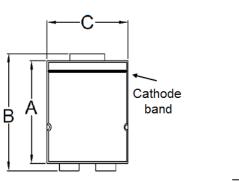


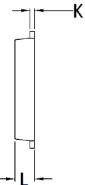
FIG.6 REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM



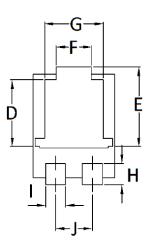


PACKAGE OUTLINE DIMENSIONS TO-277A (SMPC)

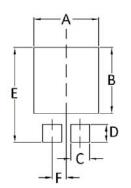




DIM.	Unit	(mm)	Unit (inch)		
Diwi.	Min	Max	Min	Max	
А	5.650	5.750	0.222	0.226	
В	6.350	6.650	0.250	0.262	
С	4.550	4.650	0.179	0.183	
D	3.540	3.840	0.139	0.151	
Е	4.235	4.535	0.167	0.179	
F	1.850	2.150	0.073	0.085	
G	3.170	3.470	0.125	0.137	
Н	1.043	1.343	0.041	0.053	
I	1.000	1.300	0.039	0.051	
J	1.930	2.230	0.076	0.088	
K	0.175	0.325	0.007	0.013	
L	1.000	1.200	0.039	0.047	



SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
А	4.80	0.189
В	4.72	0.186
С	1.40	0.055
D	1.27	0.050
E	6.80	0.268
F	1.04	0.041

MARKING DIAGRAM



- = Marking Code
- = Date Code
 - = Factory Code

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